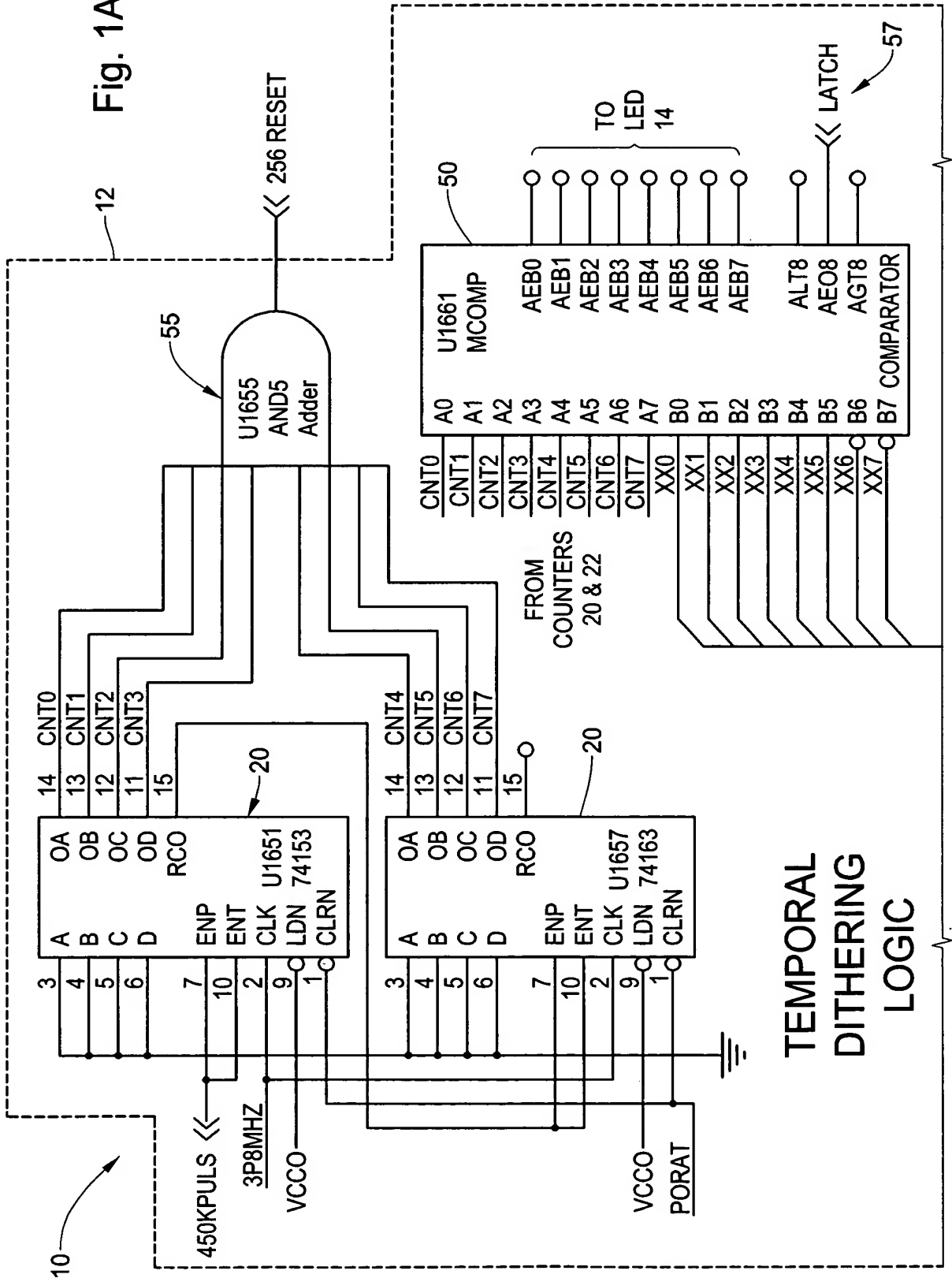


Fig. 1A



TEMPORAL  
 DITHERING  
 LOGIC

Fig. 1B

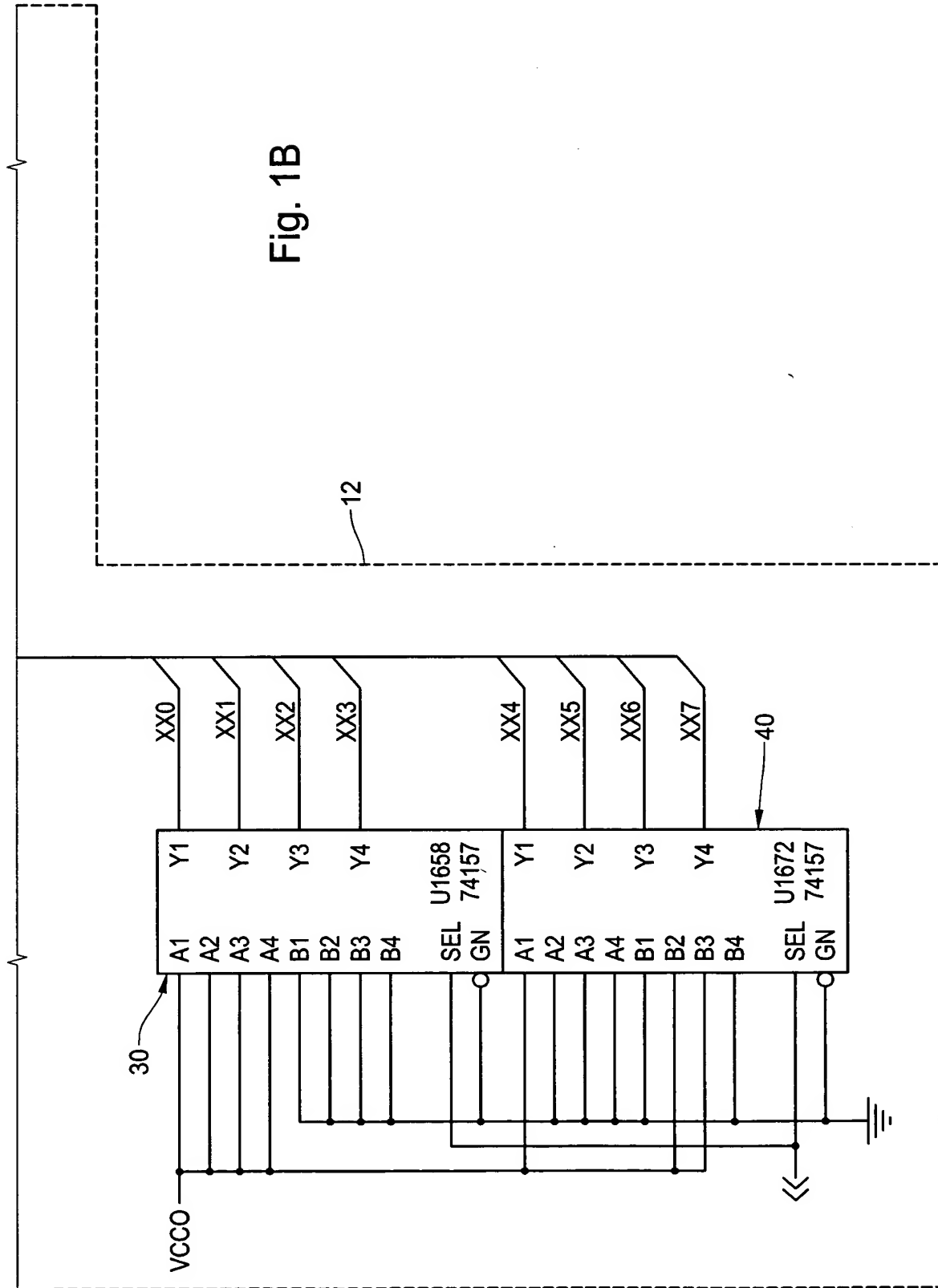


Fig. 2

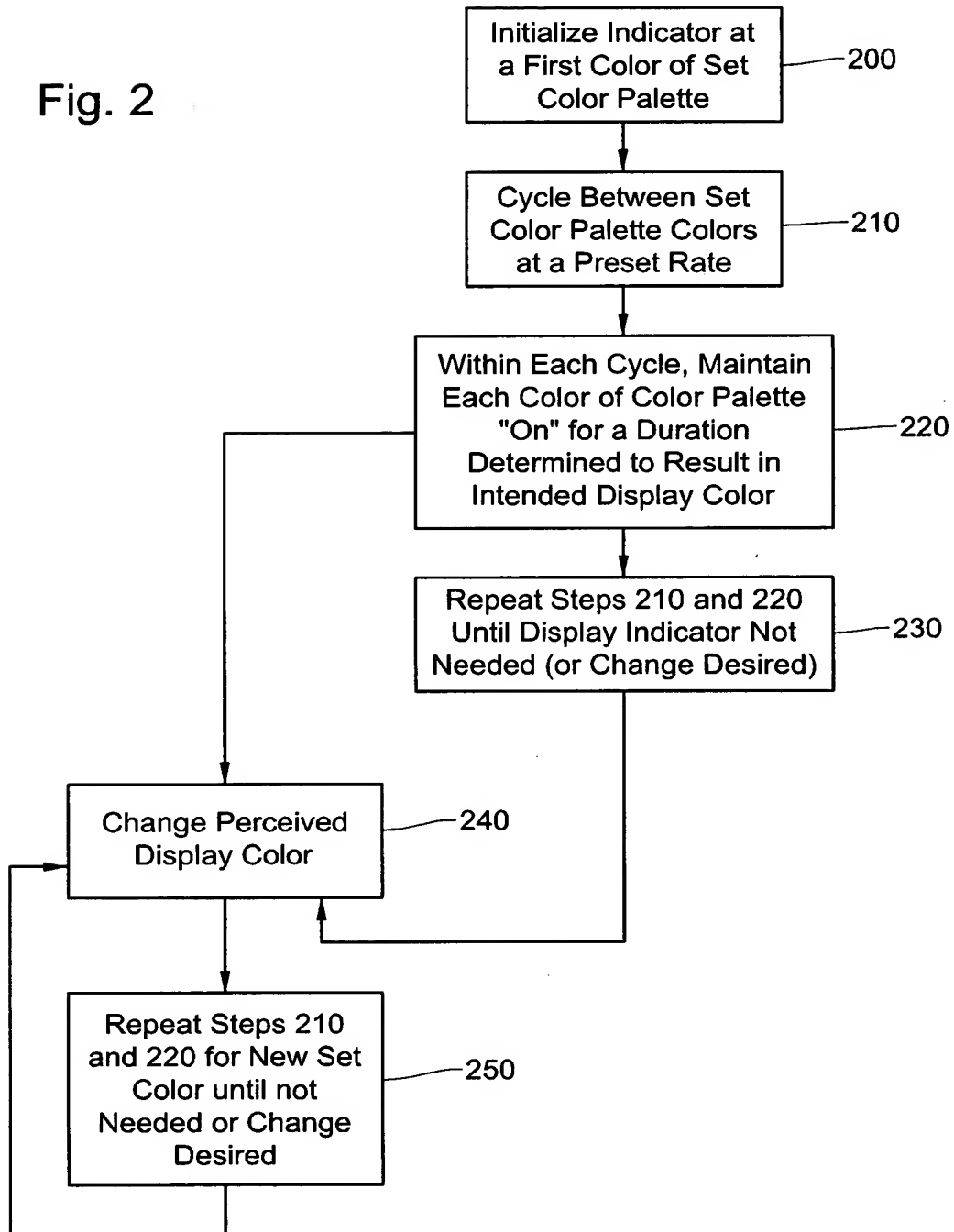
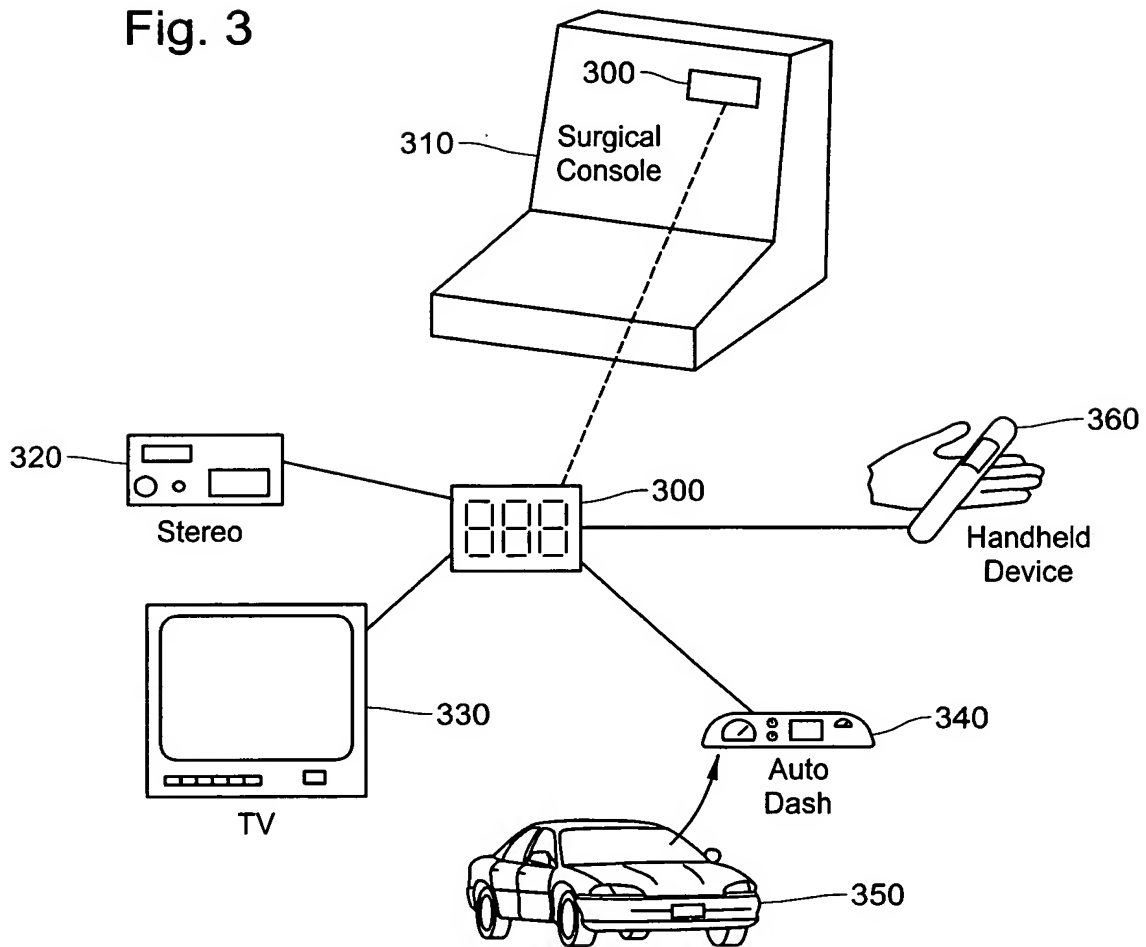


Fig. 3



400

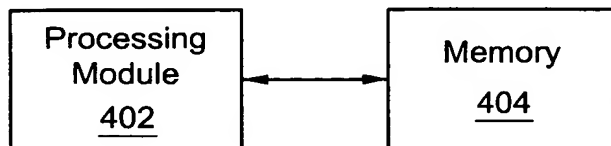


Fig. 4